

Si APD

S12023 series, etc.

Low bias operation, for 800 nm band

These are 800 nm band near-infrared Si APDs that can operate at low voltages, 200 V or less. They are suitable for applications such as FSO (free space optics) and optical rangefinders.

- **■** Stable operation at low bias
- **→** High-speed response
- → High sensitivity and low noise

- Applications

- ⇒ FSO
- Optical rangefinders

Structure / Absolute maximum ratings

			Effective	Absolute maximum ratings					
Type no.	Dimensional outline/Window material*1	Package	photosensitive area size*2	Operating temperature* ³ Topr	Storage temperature* ³ Tstg	Soldering conditions			
			(mm)	(°C)	(°C)				
S12023-02	(1)/K		φ0.2						
S12023-05	(1)/K								
S12051	(2)/L	TO-18	φ0.5						
S12086	(3)/L	10-18				260.06			
S12023-10	(1)/K		11.0	-20 to +85	-55 to +125	260 °C or less, within 10 s			
S12023-10A*4	(1)/K		φ1.0			WILIIII 10 S			
S3884	(4)/K	TO-5	φ1.5						
S2384	(5)/K	10-3	ф3.0						
S2385	(6)/K	TO-8	φ5.0						

^{*1:} K=borosilicate glass, L=lens type borosilicate glass

^{*2:} Photosensitive area in which a typical gain can be obtained

^{*3:} No dew condensation. When there is a temperature difference between a product and the surrounding area in high humidity environments, dew condensation may occur on the product surface. Dew condensation on the product may cause deterioration in characteristics and reliability.

^{*4:} This is a variant of the S12023-10 in which the device chip is light-shielded by aluminum layer except for the photosensitive area. Note: Exceeding the absolute maximum ratings even momentarily may cause a drop in product quality. Always be sure to use the product within the absolute maximum ratings.

■ Electrical and optical characteristics (Typ. Ta=25 °C, unless otherwise noted)

Type no.	Spectral response range λ	Peak*5 sensitivity wavelength λp	M=T	Quantum efficiency QE M=1 λ=800 nm	volt Vi	down age BR 00 µA	Temp. co- efficient of VBR	curi	erk* ⁵ rent	Cutoff* ⁵ frequency fc RL=50 Ω	Terminal*5 capacitance Ct	Excess*5 noise figure x	Gain M λ=800 nm
			4 - 6 - 9	45.13	Тур.	Max.		, , ,	Max.			λ=800 nm	
	(nm)	(nm)	(A/W)	(%)	(V)	(V)	(V/°C)	(nA)	(nA)	(MHz)	(pF)		
S12023-02								0.05	0.5	1000	1		
S12023-05													
S12051								0.1	1	900	2		
S12086	400 +-												100
S12023-10	400 to	800	0.5	75	150	200	0.65	0.2	2	600	6	0.3	
S12023-10A*3	1000							0.2		600	6		
S3884]							0.5	5	400	10		
S2384	1							1	10	120	40		60
S2385								3	30	40	95		40

^{*5:} Values measured at a gain listed in the characteristics table

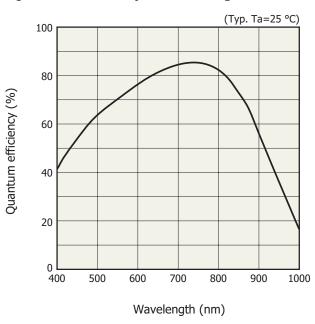
Note: Breakdown voltage can be specified by using the suffix of type number as examples shown below.

S12023-02-01: 80 to 120 V S12023-02-02: 120 to 160 V S12023-02-03: 160 to 200 V

Spectral response

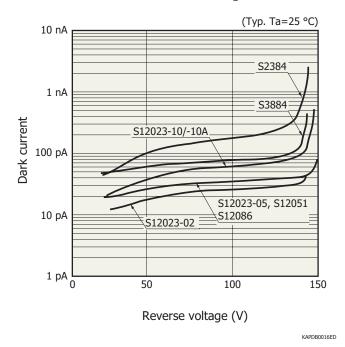
(Typ. Ta=25 °C, λ=800 nm) 60 50 Photo sensitivity (A/W) M=100 40 30 M=50 20 10 500 700 800 1000 600 900 Wavelength (nm)

- Quantum efficiency vs. wavelength

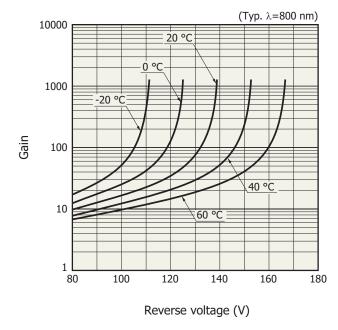


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Dark current vs. reverse voltage

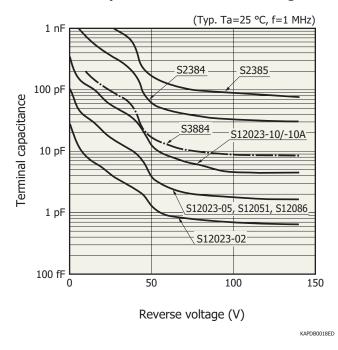


- Gain vs. reverse voltage

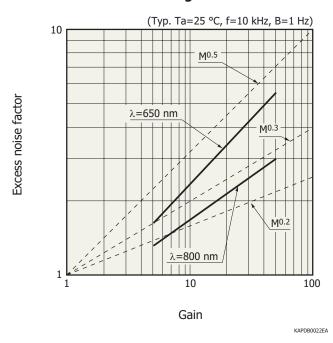


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Terminal capacitance vs. reverse voltage

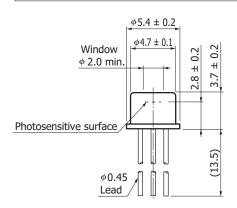


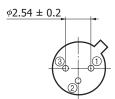
Excess noise factor vs. gain

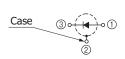


Dimensional outlines (unit: mm)

(1) S12023-02/-05/-10/-10A



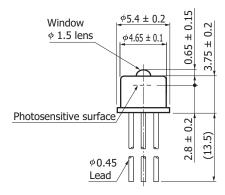


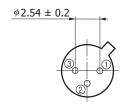


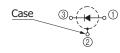
Distance from photosensitive area center to cap center $-0.2 \le X \le +0.2$ $-0.2 \le Y \le +0.2$

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(2) S12051



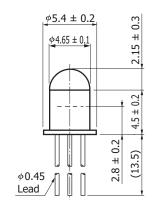


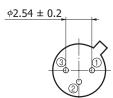


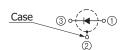
Distance from photosensitive area center to cap center $-0.2 \le X \le +0.2$ $-0.2 \le Y \le +0.2$

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(3) S12086

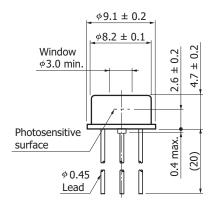


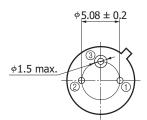


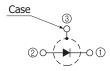


Distance from photosensitive area center to cap center $-0.2 \le X \le +0.2$ $-0.2 \le Y \le +0.2$

(4) S3884





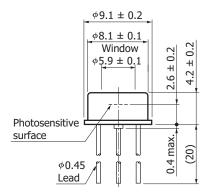


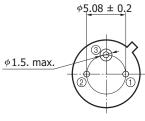
Distance from photosensitive area center to cap center $-0.3 \le X \le +0.3$ $-0.3 \le Y \le +0.3$

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(5) S2384





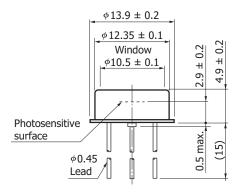
Distance from photosensitive area center to cap center -0.3≤X≤+0.3 -0.3≤Y≤+0.3

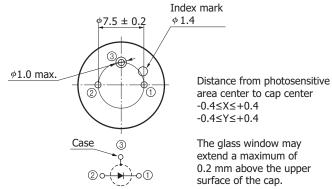


The glass window may extend a maximum of 0.2 mm above the upper surface of the cap.

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(6) S2385





KAPDA0013E

■ Replacements for previous products

Previous product (listed on the previous datasheet)*	Replacement (listed on this datasheet)
S2381	S12023-02
S2382	S12023-05
S5139	S12051
S8611	S12086
S2383	S12023-10
S2383-10	S12023-10A

^{*} Products that have been removed from this datasheet

Recommended soldering conditions

Solder temperature: 260 °C (10 s or less, once)

Solder the leads at a point at least 1 mm away from the package body.

Note: When you set soldering conditions, check that problems do not occur in the product by testing out the conditions in advance.

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S12023 series, etc.

Related information

www.hamamatsu.com/sp/ssd/doc_en.html

- Precautions
- · Disclaimer
- · Metal, ceramic, plastic package products

Information described in this material is current as of July 2022.

Product specifications are subject to change without prior notice due to improvements or other reasons. This document has been carefully prepared and the information contained is believed to be accurate. In rare cases, however, there may be inaccuracies such as text errors. Before using these products, always contact us for the delivery specification sheet to check the latest specifications.

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